

Abstracts

A Highly Integrated MMIC K-band Transmit/Receive Chip (1993 [MCS])

H. Fudem, S. Moghe, G. Dietz and S. Consolazio. "A Highly Integrated MMIC K-band Transmit/Receive Chip (1993 [MCS])." 1993 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 93.1 (1993 [MCS]): 119-122.

A highly integrated wide-band MMIC Transmit/Receive (T/R) chip was designed for both commercial and military FM CW applications. The MMIC circuit was designed using 0.25 μm pseudomorphic HEMT technology. The T/R chip has three 3-stage amplifiers, an active power divider, a diode double-balanced mixer, and a voltage controlled oscillator (VCO) all integrated on a single chip 96 X 71 mils (2.4 X 1.8 mm) in size.

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